

# SILVACO

## EDA ACADEMIC SUITE



EDA Academic Suite provides all the licenses necessary to learn all the essential EDA design functions typically found in a commercial design flow.

It enables a full design flow starting with front-end schematic capture and simulation, through back-end layout design, DRC, LVS and finally to tapeout.

The design tools in the Academic Suite can be used with publically available high quality PDKs, downloadable from the Silvaco website, [www.silvaco.com](http://www.silvaco.com).

**NOTE: For Institutional Purchase Only**



- Affordable, fixed annual fee
- Available on Linux, Solaris and Windows platforms

# Contents of One EDA Academic Suite

Product Name	Product Description
<b>AMS</b>	
Gateway	Schematic Editor
SmartSpice	Analog Circuit Simulator
Rubberband	Circuit Rubberbanding
SmartSpiceRF	RF Circuit Simulator
SmartView (4 copies)	Waveform Viewer
<b>IC CAD</b>	
Expert	Layout Editor
Guardian-DRC	Physical Verification
Guardian-LVS	Physical Verification
Guardian-NET	Physical Verification
HIPEX	Full-Chip Parasitic Extractor
<b>DIGITAL CAD</b>	
Spider	Place and Route
SILOS	Verilog Simulator

NOTE: Multicore included.

# SILVACO

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